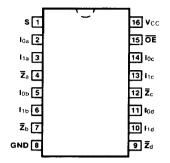
010146 54LS/74LS258A

QUAD 2-INPUT MULTIPLEXER (With 3-State Outputs)

CONNECTION DIAGRAM
PINOUT A

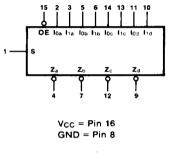


DESCRIPTION — The '258A is the same as the '258, except that the <u>output</u> drive capability is increased as indicated in the tables below. The actest limits are the same as the '258 but with the test load changed to 667 Ω and 45 pF, except for the Output Disable Time tests, whose load is 667 Ω and 5 pF. For all other information please refer to the '258 data sheet.

LOGIC SYMBOL

ORDERING CODE: See Section 9

4 5 40					
	PIN	COMMERCIAL GRADE	MILITARY GRADE	PKG	
PKGS	OUT	$V_{CC} = +5.0 \text{ V } \pm 5\%,$ $T_A = 0^{\circ} \text{ C to } +70^{\circ} \text{ C}$	V _{CC} = +5.0 V ±10%, T _A = -55° C to +125° C	TYPE	
Plastic DIP (P)	Α	74LS258APC		9B	
Ceramic DIP (D)	Α	74LS258ADC	54LS258ADM	6B	
Flatpak (F)	А	74LS258AFC	54LS258AFM	4L	



INPUT LOADING/FAN-OUT: See Section 3 for U.L. definitions

PIN NAMES	DESCRIPTION	54/74LS (U.L.) HIGH/LOW
Zn	Inverting 3-State Outputs	65/15. (25)/(7.5)

DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

SYMBOL	PARAMETER _		54/74LS		UNITS	CONDITIONS	
			Min	Max	00		
VoL	Output LOW Voltage	XM, XC XC		0.4 0.5	v	I _{OL} = 12 mA I _{OL} = 24 mA V _{CC} = Min	
los	Output Short Circuit C	-30	-130	mA	V _{CC} = Max		