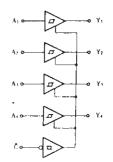
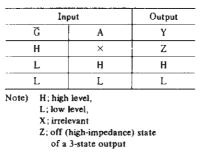
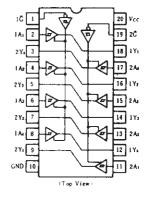
BLOCK DIAGRAM(1/2)



EFUNCTION TABLE

PIN ARRANGEMENT





ELECTRICAL CHARACTERISTICS ($Ta = -20 \sim +75^{\circ}C$)

	ltem	Symbol	Test Con	nditions		min	typ *	max	Unit
I		VIH]			2.0			v
Input volta	age	VIL						0.8	v
Hysteresis		$V_T^+ - V_T^-$	$V_{CC} = 4.75 \mathrm{V}$			0.2	0.4		V
Output voltage		Vон	$V_{CC} = 4.75 V, V_{IH} = 2 V$	Vit = 0.8V, Ion = - 3mA		2.4			v
				$V_{IL} = 0.5 V$, $I_{OH} = -15 \text{mA}$		2.0			
		Vol			<i>lot</i> = 12mA	—		0.4	v
					<i>loL</i> = 24mA	-		0.5	
Output current		Іогн	$V_{CC} = 5.25 \text{V}, V_{IH} = 2 \text{V}.$		$V_0 = 2.7 V$	-		20	μA
		lozi	$V_{IL} = 0.8V$		<i>Vo</i> =0.4V		_	- 20	
Input current		Тін	$V_{\rm CC} = 5.25 {\rm V}, V_I = 2.7 {\rm V}$				20	μA	
		hı.	$V_{CC} = 5.25 \text{V}, V_l = 0.4 \text{V}$					-0.2	mA
		h	$V_{cc} = 5.25 \text{V}, V_l = 7 \text{V}$				0.1	mA	
Short-cire	uit output current	los	$V_{CC} = 5.25 V$			40	• •	- 225	mA
C	Output "H"	Icc				-	13	23	
Supply	Output "L		$V_{\rm CC} = 5.25 V$			27	46	mA	
current	All outputs disabled]]				32	54		
Input clamp voltage		Vik	$V_{CC} = 4.75 V, I_{IN} = -18 m A$				-1.5	V	

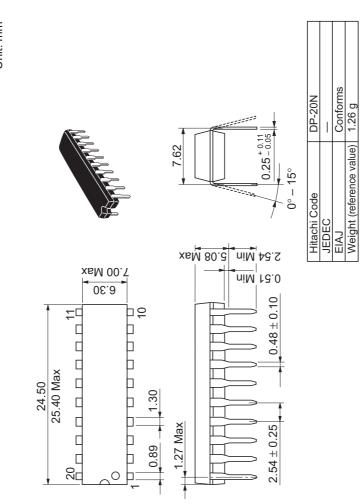
• V_{CC}=5V, Ta=25°C

** I_{CC} is measured with all outputs open.

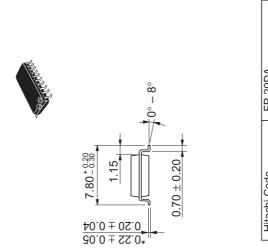
ESWITCHING CHARACTERISTICS ($V_{CC} = 5V$, $T_a = 25^{\circ}C$)

Item	Symbol	Test Conditions	min	typ	max	Unit
	tp_LH	100 H 10 10 10 H 10 H 17 H 17 H 17 H		12	18	
Propagation delay time	t PHL	$C_L = \mathbf{45pF}, R_L = 667 \ \Omega$	-	12	18	ns
A	tzL			20	30	πs
Output enable time	t ZH			15	23	ns
~	tLZ			15	25	лз
Output disable time	utput disable time	$C_L = 5 \mathrm{pF}, R_L = 667 \Omega$		10	18	ns

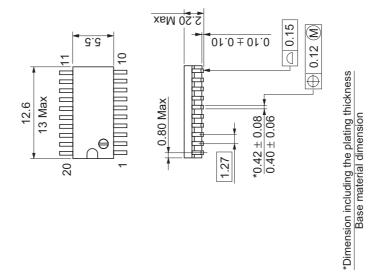
Note) Refer to Test Circuit and Waveform of the Common Item



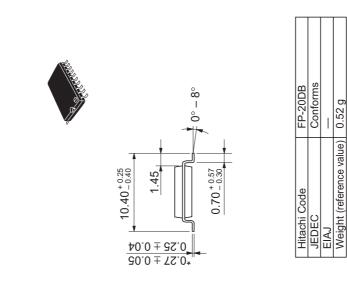
Unit: mm

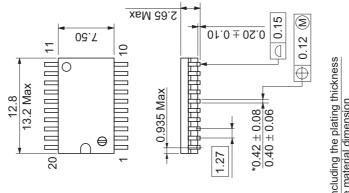


Hitachi Coda	
JEDEC	-
EIAJ	Conforms
Weight (reference value)	0.31 g



Unit: mm





*Dimension including the plating thickness Base material dimension

Unit: mm

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Hitachi, Ltd.

Semiconductor & Integrated Circuits Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan Tel: Tokyo (03) 3270-2111 Fax: (03) 3270-5109 http:semiconductor.hitachi.com/

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For further information write to: Hitachi Europe GmbH

Hitachi Semiconductor (America) Inc. 179 East Tasman Drive, San Jose CA 95134 Tel: <1> (408) 433-1990 Fax: <1>(408) 433-0223

Electronic components Group Dornacher Stra§e 3 D-85622 Feldkirchen, Munich Germany Tel: <49> (89) 9 9180-0 Fax: <49> (89) 9 29 30 00

Hitachi Europe Ltd. Electronic Components Group. Whitebrook Park Lower Cookham Road Maidenhead Berkshire SL6 8YA, United Kingdom Tel: <44> (1628) 585000 Fax: <44> (1628) 778322

Hitachi Asia Pte, Ltd. 16 Collyer Quay #20-00 Hitachi Tower Singapore 049318 Tel: 535-2100 Fax: 535-1533

Hitachi Asia I td Taipei Branch Office 3F, Hung Kuo Building. No.167 Tun-Hwa North Road, Taipei (105) Tel: <886> (2) 2718-3666 Fax: <886> (2) 2718-8180

Hitachi Asia (Hong Kong) Ltd Group III (Electronic Components) 7/F., North Tower, World Finance Centre, Harbour City, Canton Road, Tsim Sha Tsui, Kowloon, Hong Kong Tel: <852> (2) 735 9218 Fax: <852> (2) 730 0281 Telex: 40815 HITEC HX

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